

-- The above-described method and device are illustrative of the principles of the present invention. Numerous modifications and adaptations thereof will be readily apparent to those skilled in this art without departing from the spirit and scope of the present invention.--.

IN THE CLAIMS:

On page 7, at line 1, replace "Patent Claims" with --WHAT IS CLAIMED IS:--;

1. (Amended) A device [Device] for converting data sequences between frame relay (FR) format and asynchronous transfer mode (ATM) format, comprising:
 - [-] an FR communication module [(PIM)] for connecting to at least one FR communication link; [,]
 - [-] an ATM communication module for connecting to an ATM communication link; [,]
 - [-] a central computer [(FP)] for controlling said [the] FR communication module and said [the] ATM communication module; [,] and
 - [-] a buffer memory [(PSSM)], which is connected via an internal communication link to said [the] central computer [(FP)], said [the] FR communication module [(PIM)] and said [the] ATM communication module.
2. (Amended) A conversion [Conversion] device according to claim 1, wherein said [characterized in that the] internal communication link is a bus link.

4. (Amended) A conversion [Conversion] device according to claim 1,
wherein said internal communication link comprises [one of the claims 1 through
5 3, characterized in that] two separate bus links [are provided] for driving said
[the] FR communication module [(PIM)].

6. (Amended) A conversion [Conversion] device according to claim 1,
wherein said [one of the claims 1 though 5,
15 characterized in that the] buffer memory [(PSSM) is divided into] comprises a
reception unit and a transmission unit.

8. (Amended) A method [Method] for converting data sequences from an FR format into an ATM format comprising the steps of: [by means of]
25 providing a conversion device, comprising an FR communication module [(PIM)

[illegible]

Q3
Cont.

Cont.